



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Yi Ding
Assignee: Mosel Vitelic, Inc.
Title: Arrays of Nonvolatile Memory Cells Wherein Each Cell Has Two Conductive Floating Gates
Application No.: 10/632,007 Filing Date: July 30, 2003
Examiner: Ida M. Soward Group Art Unit: 2822
Docket No.: M-15223 US

San Jose, California
April 16, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(c)

Dear Sir:

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed except for United States Patents and United States Published Patent Applications.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be material to patentability as defined in § 1.56(b).

Please charge the fee of \$180.00 as set forth in 37 CFR 1.17(p) to deposit account 50-2257. Please charge any amount underpaid, or credit the amounts overpaid, to the same deposit account. This paper is being submitted in duplicate.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on April 16, 2004.

Michael Shenker
Attorney for Applicant

4-16-04
Date of Signature

Respectfully submitted,

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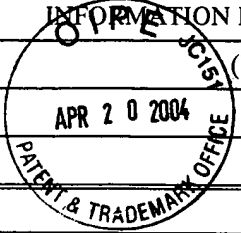
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Serial No. 10/632,007

U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Serial No.		
				M-15223 US		10/632,007		
 INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				Applicant				
				Yi Ding				
				Filing Date		Group		
				July 30, 2003		2822		
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	6,420,231	16 Jul. 2002	Harari et al.				
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AI							
	AJ							
	AK							
	AL							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AM	United States Patent Application No. 10/798,475, entitled "Fabrication of Conductive Lines Interconnecting Conductive Gates in Nonvolatile Memories and Non-Volatile Memory Structures," Filed on March 10, 2004; Attorney Docket No. M-15296 US.						
	AN	United States Patent Application No. 10/797,972, entitled "Fabrication of Conductive Lines Interconnecting First Conductive Gates in Nonvolatile Memories Having Second Conductive Gates Provided By Conductive Gates Lines, Wherein The Adjacent Conductive Gate Lines For The Adjacent Columns Are Spaced From Each Other, And Non-Volatile Memory Structures," Filed on March 10, 2004; Attorney Docket No. M-15297 US.						
	AO							
	AP							
Examiner			Date Considered					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								